# Simply flexible – Bandwidth management with adjustable pixel clock

You are using a multi-camera system or a high resolution camera? You want to use the full bandwidth of your camera interface without losing any image?

For IDS industrial cameras, you can set the pixel clock using the IDS Software Suite. So you can have a direct influence on the needed bandwidth of a camera.

# Background

The basic parameter for camera timing is the pixel clock. It determines the speed at which the sensor cells can be read out. In general, the pixel clock is set once when opening the camera and will not be changed.

The possible range of settings for the frame rate depends on the currently selected pixel clock. You can select a lower frame rate without changing the pixel clock. To set a higher frame rate, however, you need to increase the pixel clock.

The exposure time depends on the currently selected frame rate and is preset to its reciprocal value. You can select a shorter exposure time without changing the frame rate. To set a longer exposure time, however, you need to reduce the frame rate.

# Setting the pixel clock

Open the camera properties in uEye Cockpit via "uEye > Properties". You can set the pixel clock, frame rate, and exposure time in the "Camera" tab.

Note that, if you change the pixel clock, the setting ranges for frame rate and exposure time also changes. If you change a parameter, the following order is recommended:

- 1. Change the pixel clock
- 2. Set the frame rate
- 3. Set the exposure time

There are three approaches for setting the pixel clock: the camera should be operated as fast as possible or with long exposure or the camera should hold a defined frame rate.

# High frame rates

A high pixel clock allows short exposure times and higher frame rates. In case of rolling shutter sensors, you can reduce the rolling shutter effect with a high pixel clock.



# TechTip: Bandwidth management with adjustable pixel clock

Info	Camera	Image	Size	Format	Color	LUT	Trigger
Timing							
Camer Camer Senso	Camera peak bandwidth: Camera average bandwidth: Sensor (may, bandwidth):			399.0 MB/s 375.2 MB/s 344.0 MP/s			
Pixel c	lock					344	* *
Ext	timum 38 ended	8 MHz	AL	344 MHz Auto pixel clock test period (s)			×
Frame (Freeru Hol Ma Aut	rate m) 2. d 2. x	00 fps			152.21 fps	152.21	V
Exposu Hol Ma V Aut	ure time — Id 0. x o	025 ms			6.529 ms	0.805	A
E Lor	ng-term e increment			ſ	Dual exposure	No	•
				Defa	ault	Close	

Setting range of frame rate and exposure time at maximum pixel clock (UI-336x)

# Minimum pixel clock for long exposure

A low pixel clock allows longer exposure times and causes a lower CPU load.

Info Camera	Image	e Size	Format	Color	LUT	Trigger				
Timing										
Camera peak ban										
Camera average b	Camera average bandwidth:			4.9 MB/s						
Sensor (max. band	Sensor (max. bandwidth):			38.0 MP/s						
Pixel clock										
Cating an					- 38	-				
Extended	38 MHz	MHz 344 MHz			z					
Extended		AL	Auto pixel clock test period (s)			-				
Frame rate	D				2.00					
(Freerun)	2 00 fpe			16.87 fo						
Max	Max			10.071p	3					
Auto										
Exposure time					499.73					
Hold	0.084 ms			499.73 m	s					
Max										
Auto										
Long-term										
Fine increment				Dual exposur	No	•				
			Def	ault	Clos	e				

Setting range of frame rate and exposure time at minimum pixel clock (UI-336x))

#### Setting the optimum pixel clock for a defined frame rate

In this case, you proceed in a slightly different order:

- 1. Set the frame rate to the desired value.
- 2. Enable the "Hold" option.
- 3. Reduce the pixel clock as far as possible, so that the selected frame rate is still achieved.

As the pixel clock has a direct effect on the frame rate, we recommend not setting the pixel clock any higher than necessary to achieve the desired frame rate. The goal should be that the values for the camera peak bandwidth and average bandwidth are close together.

Info	Camera	Image	Size	Format	Color	LUT	Trigger		
Timing									
Camera Camera	Camera peak bandwidth: Camera average bandwidth:			134.6 MB/s 123.2 MB/s					
Sensor	Sensor (max. bandwidth):			MP/s					
Pixel cl	ock _					116			
Opt	imum 3	8 MHz			344 MH;	2			
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Frame ( (Freeru V Hole Max Auto	rate n) – d 2 c o	2.00 fps			51.44 fps	50.00	V		
Exposu Hole	rretime ⊢ d O ¢	).037 ms			19.912 ms	2.421	A V		
Lon	o g-term e increment			C	Dual exposure	No	•		
				Defa	ault	Clos	e		

Setting the pixel clock for a fixed frame rate (UI-336x)

# **Application areas**

Using the adjustable pixel clock, the camera can optimally be adjusted to the available bandwidth. In particular the bandwidth management is greatly simplified in multi-camera systems because each camera can be set easily and individually.

Additionally, uEye camera models with CCD sensors are calibrated for each pixel clock to get the best image quality.

# Summary

The pixel clock limit values can vary, depending on the camera model, interface, and operating mode. The preset default value is a compromise between the image quality and the frame rate.

An excessive pixel clock can cause delays or transmission errors. If the data is read from the sensor at a higher speed (high pixel clock), you will also need a

faster transmission over the data connection. Thus, by controlling the pixel clock, you can also influence the bandwidth required for a camera.

The pixel clock influences the connected load and consequently the temperature inside the camera. At a lower pixel clock the camera temperature increases less.

Using IDS Software Suite, you can easily adjust the pixel clock for almost all IDS camera models. With the uEye API function "is\_PixelClock" or the uEye .NET class "PixelClock" you can query and set the pixel clock.

For further information on setting and programming the pixel clock, refer to the uEye manual at <u>http://en.ids-imaging.com/manuals-ueye.html</u>.

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